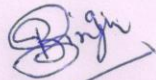


DATE SHEET FOR M. TECH MST-I

First Semester M.Tech 2017-19				
Date and Time	Subject VLSI Design	Subject Embedded Systems	Subject EPDT	Subject IT-Part Time
18-09-2017 10am-11am	Research Methodology	Research Methodology	Research Methodology	Research Methodology
19-09-2017 10am-11am	VLSI Design Concepts	Advanced Digital System Design	Advanced Digital System Design	
20-09-2017 10am-11am	Hardware Description Languages	Data Communication Network	Electronic Product Design	Advanced Software Engineering
21-09-2017 10pm-11am	VLSI Technology	Software Technology	Advanced Microcontroller Embedded System	
22-09-2017 10am-11pm	Design of Analog/ Mixed Mode VLSI Circuits	Designing with Power Devices	Designing with Power Devices	Advanced Database Systems
Third Semester M.Tech 2016-18				
19-09-2017 2pm-3pm	Sensor Technology and MEMS		Sensor Technology and MEMS	
21-09-2017 2pm-3pm	Process and Device Characterization & Measurements		Embedded Wireless Sensor Network	

Note: Exam will be conducted in Lab No. 39



Dr. Balwinder Singh
(Co-Ordinator, ACSD)